

# RVP8™ Specifications

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## RVP8™ IF Digitizer Module (installed in receiver cabinet) and IF Processing

<b>IF Inputs</b>	Dual IF signal channels for dual pol or ultra-wide dynamic range applications: 50 Ohm, + 6.5 dBm Transmit burst channel for AFC and digital phase locking: 50 Ohm, + 6.5 dBm
<b>IF Ranges</b>	Selectable: 12-34 MHz, 38-70 MHz
<b>Dynamic Range</b>	85-100 dB depending on matched filter (e.g. 96 dB for 0.5 MHz matched filter). Optional 20 dB ultra wide dynamic range extension using 2 <sup>nd</sup> input channel
<b>A/D Conversion</b>	14 Bits at 72 MHz with jitter <2.5 picosec
<b>AFC Output</b>	Control Voltage - 10V to +10V, nominal $\pm 14$ MHz span or 25-bit digital control
<b>IFD Link</b>	Standard CAT 5-E gigabit connection
<b>IF Band Pass Filters</b>	Digital FIR with selectable impulse response and pass band. Built-in filter design software with graphical display
<b>Impulse Response</b>	Up to 80 microseconds (for optional pulse compression)
<b>Range Resolution</b>	Arbitrary bin spacing to within $\pm 1$ m, minimum of 25 m
<b>Maximum Range</b>	8192* resolution, e.g., 1024 km @ 125-m resolution
<b>Number of range bins</b>	Up to 3096 spaced within an 8192-bit range mask
<b>Phase Stability</b>	Klystron: Better than 0.1 degrees Magnetron: Better than 0.5 degrees (for 1.0 microsecond pulse)

## RVP8™ IF and Signal Processing Unit

<b>Processing Modes</b>	Pulse Pair DFT/ FFT, GMAP™, Random Phase 2 <sup>nd</sup> trip filtering/ recovery Optional: ZDR, RHOHV and KDP in pulse pair mode
<b>Azimuth (Pulse) Averaging</b>	2 to 1024
<b>Corrections</b>	Range normalization (normally $1/R^2$ ), gaseous attenuation

<b>Velocity De-aliasing</b>	By dual PRF technique in ratios of 2:3, 3:4, or 4:5 for 2X, 3X or 4X de-aliasing
<b>Range De-aliasing</b>	By random phase technique for magnetron. Klystron can have optimized phase coding controlled by RVP8/Tx™
<b>Clutter Filters</b>	Fixed or automatic adaptive width GMAP™ filtering to > 60 dB clutter cancellation
<b>Outputs</b>	dBZ: 8 or 16 bits; V: 8 or 16 bits; W: 8 or 16 bits; I & Q: 16 bits; FFT:16 bits Optional Polarization parameters 8 or 16 bits
<b>Performance Options</b>	Dual SMP Pentium processors easily upgradeable as faster processors become available Gigabit networking for multiple processor expansion
<b>Host Interface</b>	10/100/1000T Ethernet
<b>Configuration Interface</b>	Using optional keyboard, mouse, monitor or via network workstation
<b>Physical and Environment</b>	
<b>Packaging</b>	IF Digitizer Module: 30 x 109 x 236 mm mounted in receiver cabinet RVP8™ Main Chassis: 4U rackmount chassis with >40 GB Disk, DVD+RW. Other PC configurations available
<b>Input Power</b>	100-240 VAC 47-63 Hz
<b>Power Consumption</b>	IF Digitizer: 24 Watts; RVP8™: 70 Watts
<b>Environmental</b>	0° C to 50° C operating, 0 to 95% (non condensing) R.H.
<b>Reliability</b>	Digitizer, RVP8™:>40,000 Hours MTBF (estimated from RVP7™ field data)

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